This exam includes five problems. Note that the last one is at the other side of the paper. Each problem gives 10 points at maximum.
1.

Answer shortly, with a few words, to the following questions.
a) Name the following symbols related to feedback: $A_{F}, A, \beta$, and $A \beta$.
b) You need to know the frequency limit of a transistor biased as an amplifier. Which transistor model do you use? Draw the model.
c) How can you recognize an active circuit from its scattering parameters?
d) When analyzing digital systems, what does multiplying by $z^{-1}$ mean?
2.


An acoustic sensor is represented by $v_{s}$ in the figure. The band-pass filter in the figure is specified to pass an input signal with its specified bandwidth. Determine the bandwidth and center frequency of the circuit when the op-amp is assumed ideal.
Hint: Determine first the transfer function $\frac{v_{\text {out }}}{v_{\mathrm{s}}}$.
3.


The operating point of the transistor is $V_{G S}=3 \mathrm{~V}$, $V_{D S}=10 \mathrm{~V}$ and $I_{D}=4 \mathrm{~mA}$.
a) Calculate the open-circuit voltage gain for the CSamplifier.
b) Calculate the input impedance and the output impedance of the CS-amplifier.
You can neglect the channel-length modulation in this exercise. Capacitances $C$ are large at the frequency of the input signal.
Hint: Start by drawing the small signal model.

$$
\begin{array}{rlrc}
R_{B 1} & =10 \mathrm{k} \Omega & R_{B 2}=30 \mathrm{k} \Omega & R_{D}=2 \mathrm{k} \Omega \\
R_{S} & =500 \Omega & V_{D D}=20 \mathrm{~V} & k_{n}^{\prime} \frac{W}{L}=2 \mathrm{~mA} / \mathrm{V}^{2} \\
V_{t} & =1 \mathrm{~V} . & &
\end{array}
$$

An electronic amplifier has the circuit shown in the figure. Determine the impedance parameters for the circuit.

$$
\begin{aligned}
& z_{11}=\left.\frac{v_{1}}{i_{1}}\right|_{i_{2}=0} \quad z_{12}=\left.\frac{v_{1}}{i_{2}}\right|_{i_{1}=0} \\
& z_{21}=\left.\frac{v_{2}}{i_{1}}\right|_{i_{2}=0} \quad z_{22}=\left.\frac{v_{2}}{i_{2}}\right|_{i_{1}=0}
\end{aligned}
$$

5. 

Answer this question by writing and drawing on this paper and returning it.
a) Name the type of filter for each of the figures.
b) Mark $f_{0}, f_{L}$ and $f_{H}$ on the figure(s) (where applicable!).
c) What are the locations of poles and zeros in each case?

Note: all filters are of the second-order!

Filter type is: $\qquad$


Filter type is: $\qquad$


Filter type is: $\qquad$


Filter type is: $\qquad$

$\qquad$ Student number: $\qquad$
0.1

Answer shortly, with a few words, to the following questions.
a) Name the following symbols related to feedback: $A_{F}, A, \beta$, and $A \beta$.
b) You need to know the frequency limit of a transistor biased as an amplifier. Which transistor model do you use? Draw the model.
c) How can you recognize an active circuit from its scattering parameters?
d) When analyzing digital systems, what does multiplying by $z^{-1}$ mean?
a)

- $A_{F}: A_{F}=\frac{A}{1+A \beta}$, closed loop gain
- $A$ : Open loop gain
- $\beta$ : Feedback
- $A \beta$ : Loop Gain
b)


$$
\begin{aligned}
g_{m} & =\sqrt{2 I_{D} k_{n}^{\prime} \frac{W}{L}} \\
& =k_{n}^{\prime} \frac{W}{L}\left(V_{G S}-V_{t n}\right) \\
r_{d s} & \approx \frac{1}{\lambda I_{D}}
\end{aligned}
$$

c) If the magnitude of any $S$ parameter is larger than 1 or

$$
\left|S_{11}\right|^{2}+\left|S_{21}\right|^{2}>1
$$

d) Multiplying by $z^{-1}$ models a delay of one time pediod of sampling frequency.
0.2


An acoustic sensor is represented by $v_{s}$ in the figure. The band-pass filter in the figure is specified to pass an input signal with its specified bandwidth. Determine the bandwidth and center frequency of the circuit when the op-amp is assumed ideal.
Hint: Determine first the transfer function $\frac{v_{\text {out }}}{v_{\mathrm{s}}}$.


Same voltage in input nodes of opamp. Node B is at the ground potential and the corresponding column can be removed.

$$
\begin{gathered}
{\left[\begin{array}{cc}
G_{1}+s C_{1} & 0 \\
-s C_{1} & -G_{2}-s C_{2}
\end{array}\right]\left[\begin{array}{c}
U_{\mathrm{A}} \\
U_{\mathrm{C}}
\end{array}\right]=\left[\begin{array}{c}
G_{1} v_{s} \\
0
\end{array}\right]} \\
H(s)=\frac{v_{\mathrm{out}}}{v_{s}}=-\frac{-s C_{1} G_{1}}{\left(G_{1}+s C_{1}\right)\left(G_{2}+s C_{2}\right)}=\frac{s C_{1} G_{1}}{s^{2} C_{1} C_{2}+s\left(C_{1} G_{2}+C_{2} G_{1}\right)+G_{1} G_{2}}=\frac{h(s)}{s^{2}+s \frac{\omega_{0}}{Q}+\omega_{0}^{2}} \\
\omega_{0}=\sqrt{\frac{G_{1} G_{2}}{C_{1} C_{2}}} \\
\Delta \omega=\frac{\omega_{0}}{Q}=\frac{C_{1} G_{2}+C_{2} G_{1}}{C_{1} C_{2}}
\end{gathered}
$$

0.3


The operating point of the transistor is $V_{G S}=3 \mathrm{~V}$, $V_{D S}=10 \mathrm{~V}$ and $I_{D}=4 \mathrm{~mA}$.
a) Calculate the open-circuit voltage gain for the CSamplifier.
b) Calculate the input impedance and the output impedance of the CS-amplifier.
You can neglect the channel-length modulation in this exercise. Capacitances $C$ are large at the frequency of the input signal.
Hint: Start by drawing the small signal model.

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\begin{array}{rlrc}
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R_{S} & =500 \Omega & V_{D D}=20 \mathrm{~V} & k_{n}^{\prime} \frac{W}{L}=2 \mathrm{~mA} / \mathrm{V}^{2} \\
V_{t} & =1 \mathrm{~V} . & &
\end{array}
$$

a) Small signal:


Now we can calculate the parameters with the DC-operation point values:

$$
\begin{aligned}
g_{m} & =\sqrt{2 I_{D} k_{n}^{\prime} \frac{W}{L}}=k_{n}^{\prime} \frac{W}{L}\left(V_{G S}-V_{t n}\right)=4 \mathrm{mS} \\
r_{d s} & \approx \frac{1}{\lambda I_{D}} \approx \infty
\end{aligned}
$$

because we were told to ignore the channel length modulation, so that means there is no resistor $r_{d s}$ in our circuit.
Next, we will calculate the open circuit voltage (as we have no load, the output of the open circuit will just be the same as $v_{\text {out }}$ in our figure):

$$
\begin{align*}
& v_{\text {out }}=-g_{m} R_{D} v_{g s}=-g_{m} R_{D}\left(v_{g}-v_{s}\right)=-g_{m} R_{D}\left(v_{i n}-0\right)=-g_{m} R_{D} v_{i n}  \tag{1}\\
& A_{v o}=\frac{v_{\text {out }, \text { open }}}{v_{\text {in }}}=-g_{m} R_{D}=-8 \tag{2}
\end{align*}
$$

b) Using the figure from a-part, we get

$$
\begin{align*}
& R_{\text {in }}=R_{B 1} \| R_{B 2}=7.5 \mathrm{k} \Omega  \tag{3}\\
& R_{\text {out }}=R_{D}=5 \mathrm{k} \Omega \tag{4}
\end{align*}
$$

0.4


An electronic amplifier has the circuit shown in the figure. Determine the impedance parameters for the circuit.

$$
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& z_{21}=\left.\frac{v_{2}}{i_{1}}\right|_{i_{2}=0} \quad z_{22}=\left.\frac{v_{2}}{i_{2}}\right|_{i_{1}=0}
\end{aligned}
$$

Assume first, $i_{2}=0$ :


$$
\begin{gathered}
z_{11}=\left.\frac{v_{1}}{i_{1}}\right|_{i_{2}=0}=R_{1}+R_{3} \\
z_{21}=\left.\frac{v_{2}}{i_{1}}\right|_{i_{2}=0}=R_{3}
\end{gathered}
$$

Assume next, $i_{1}=0$ :


Applying the Kirchhoff's current law, the current through the resistor $R_{3}$ is $(1+\alpha) i_{2}$.

$$
\begin{gathered}
z_{12}=\left.\frac{v_{1}}{i_{2}}\right|_{i_{1}=0}=(1+\alpha) R_{3} \\
z_{22}=\left.\frac{v_{2}}{i_{2}}\right|_{i_{1}=0}=R_{2}+(1+\alpha) R_{3}
\end{gathered}
$$

Answer this question by writing and drawing on this paper and returning it.
a) Name the type of filter for each of the figures.
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c) What are the locations of poles and zeros in each case?

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Name: $\qquad$ Student number: $\qquad$
a) Low-pass filter.

Two poles at $f_{0}=500 \mathrm{~Hz}$ (two zeroes at $\infty$ ).

b) High-pass filter.

Two zeroes at 0 and two poles at $f_{0}=600 \mathrm{~Hz}$.

c) Band-pass filter.

One zero at 0 , two poles at $f_{0}=10 \mathrm{kHz}$ (one zero at $\infty$ ).

d) Band-stop filter.

One pole at $f_{L}=1 \mathrm{kHz}$, two zeroes at $f_{0}=10 \mathrm{kHz}$ and one pole at $f_{H}=100 \mathrm{kHz}$.


